

**REMARKS**

Claims 1-14 are pending in this application. Claims 1 and 2 have been amended, with support found in the specification at pages 10-13. No new matter has been added.

**I. Rejections under 35 U.S.C. §102(e)**

Claims 1-14 are rejected under 35 U.S.C. §102(e) as being anticipated by Hatano (U.S. Patent Publication No. 2003/0230938). Applicants respectfully traverse this rejection.

Claims 1 and 2 have been amended to recite a capacitor that is connected directly to the terminal of the primary winding. The reference does not disclose the subject matter recited in claims 1 and 2.

The Office Action asserts that Hatano discloses all elements recited in claims 1 and 2, referring to Figs. 3 and 14. However, both Figs. 3 and 14 of Hatano do not indicate a capacitor in the electric circuit system. The Office Action fails to identify any element in Figs. 3 and 14 of Hatano that indicates a capacitor. In fact, there is no capacitor in Figs. 3 and 14. Thus, Figs. 3 and 14 of Hatano do not describe a capacitor, much less a capacitor that is directly connected to a terminal of the primary winding.

Thus, for at least these reasons, claims 1 and 2 are patentable over Hatano. Further, the dependent claims are likewise patentable over the applied references for at least the reasons discussed above in connection with claims 1 and 2, as well as for the additional features they recite. Applicants respectfully request that the 35 U.S.C. §102 rejection be withdrawn.

**II. Rejection Under 35 U.S.C. §103**

Claim 2 is rejected under 35 U.S.C. §103 as being unpatentable over Sula (U.S. Patent No. 6,608,768). Applicants respectfully traverse this rejection.

Claim 2 has been amended to recite a capacitor that is directly connected to a terminal of the primary winding. Sula does not disclose a capacitor that is directly connected to a

terminal of the primary winding. The Office Action asserts that element C4 in Fig. 3 of Sula indicates a capacitor that is parallel to a primary winding. However, capacitor C4 is connected directly to a diode D2, and is not directly connected to a terminal of the primary winding.

Thus, for at least these reasons, claim 2 is patentable over Sula. Further, the dependent claims are likewise patentable over the applied references for at least the reasons discussed above in connection with claim 2, as well as for the additional features they recite. Applicants respectfully request that the 35 U.S.C. §103 rejection be withdrawn.

**III. Double Patenting Rejection**

Applicants have amended claims 1 and 2 to overcome the rejection on the grounds of non-statutory obviousness type double patenting as being unpatentable over claims 1-21 of U.S. Patent No. 7,084,528. Claims 1 and 2 are amended to recite that the capacitor is directly connected to terminal of the primary winding, rendering the Double Patenting rejection moot. Applicants respectfully request that the non-statutory obviousness type double patenting rejection be withdrawn.

**IV. Conclusion**

In view of the foregoing, it is respectfully submitted that this application is in condition for allowance. Favorable reconsideration and prompt allowance of claims 1-14 are earnestly solicited.

Should the Examiner believe that anything further would be desirable in order to place this application in even better condition for allowance, the Examiner is invited to contact the undersigned at the telephone number set forth below.

Respectfully submitted,



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